

PATENT NUMBER

JC973 U.S. PTO
09/19/4365
08/31/01

| Class | Subclass |
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ISSUE CLASSIFICATION

U.S. UTILITY Patent Application

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| 12W O.I.P.E. SCANNED <u>AC</u> Q.A. <u>1004</u> | PATENT DATE |
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| APPLICATION NO. 09/944365 | CONT/PRIOR F | CLASS 257 | SUBCLASS 350 | ART UNIT 2811 | EXAMINER T. T. B. W. |
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APPLICANTS

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Hisashi Hasegawa
Sumio Koike
Kazutoshi Ishii

TITLE

Complementary MOS semiconductor device and method of manufacturing the same

PTO-2040
12/99

| ISSUING CLASSIFICATION | | | | | | | | | | |
|-------------------------------------|--|----------|--|--------------------|-----------------------------------|--|--|--|--|--|
| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | | |
| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) _____ (Date) | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____ | _____ (Primary Examiner) _____ (Date) | | ISSUE FEE | |
| | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) _____ (Date) | | ISSUE BATCH NUMBER | |
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